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FIG. 2 is a schematic diagram of a device in accordance with the present invention.

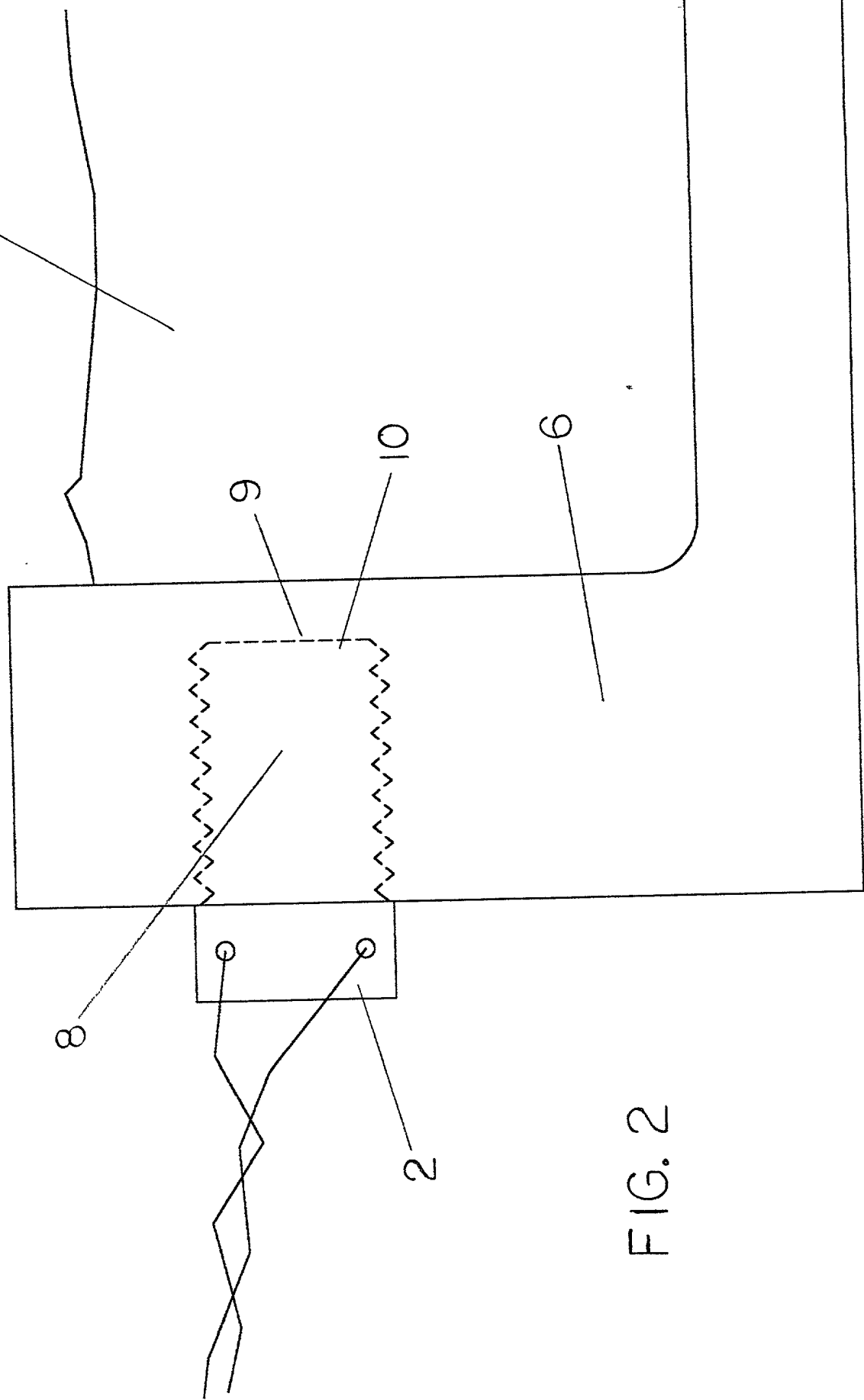
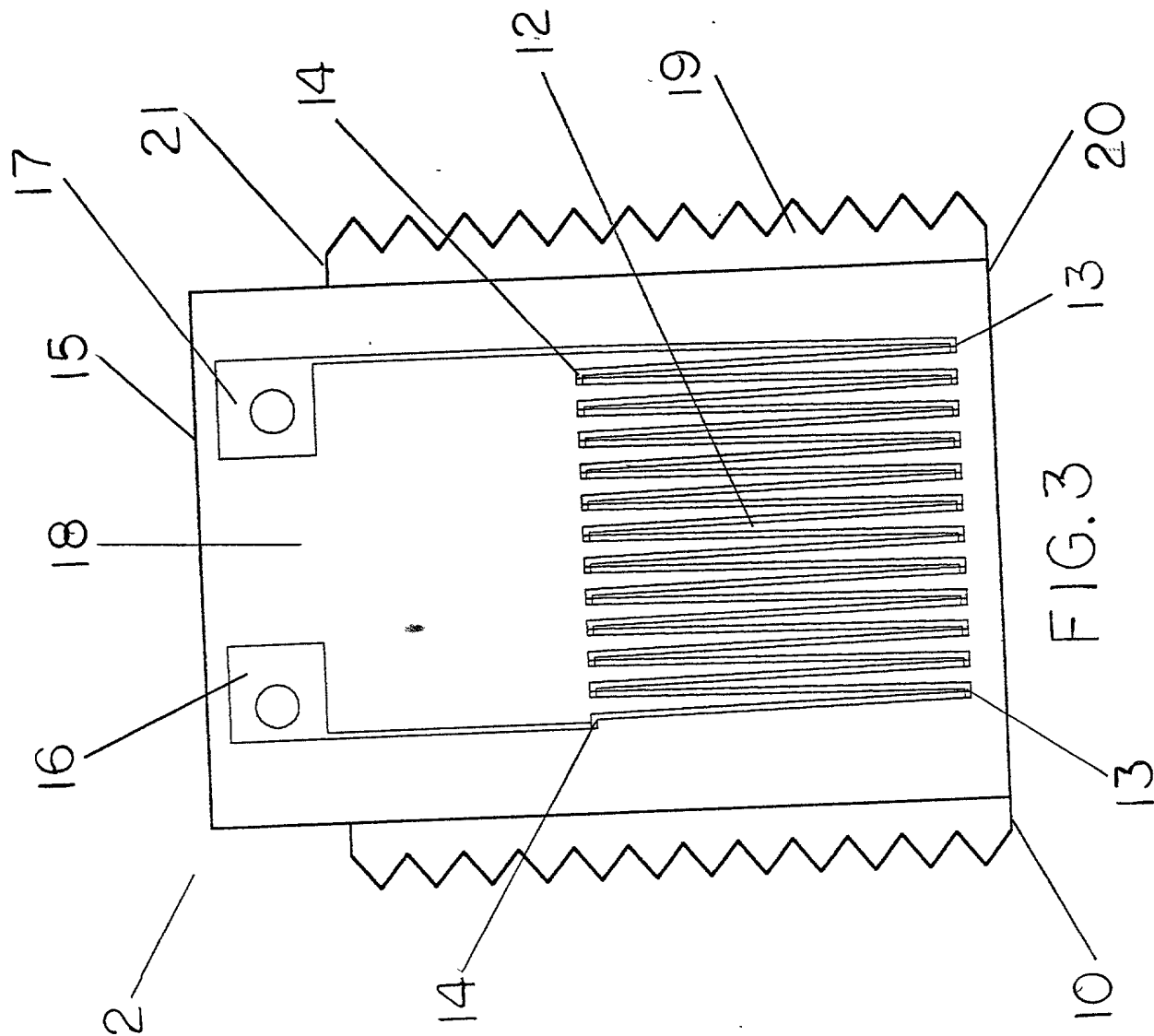
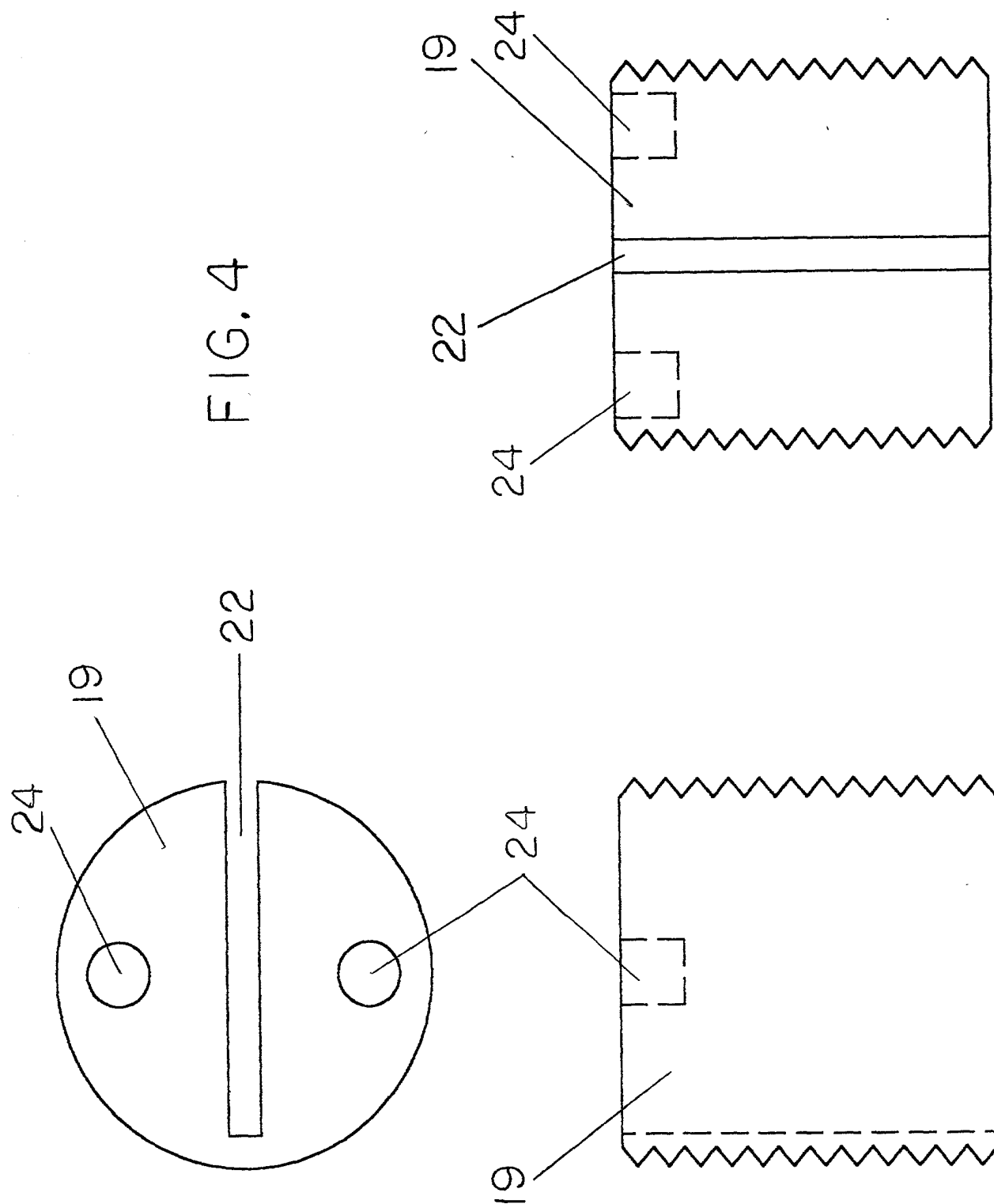


FIG. 2





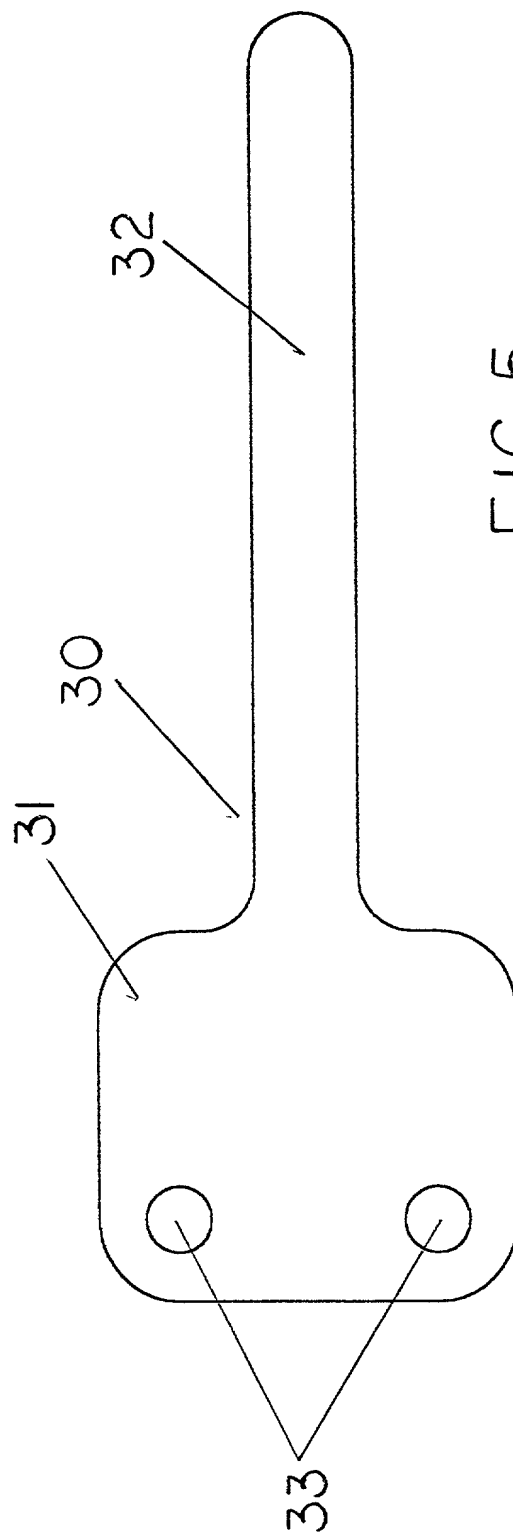
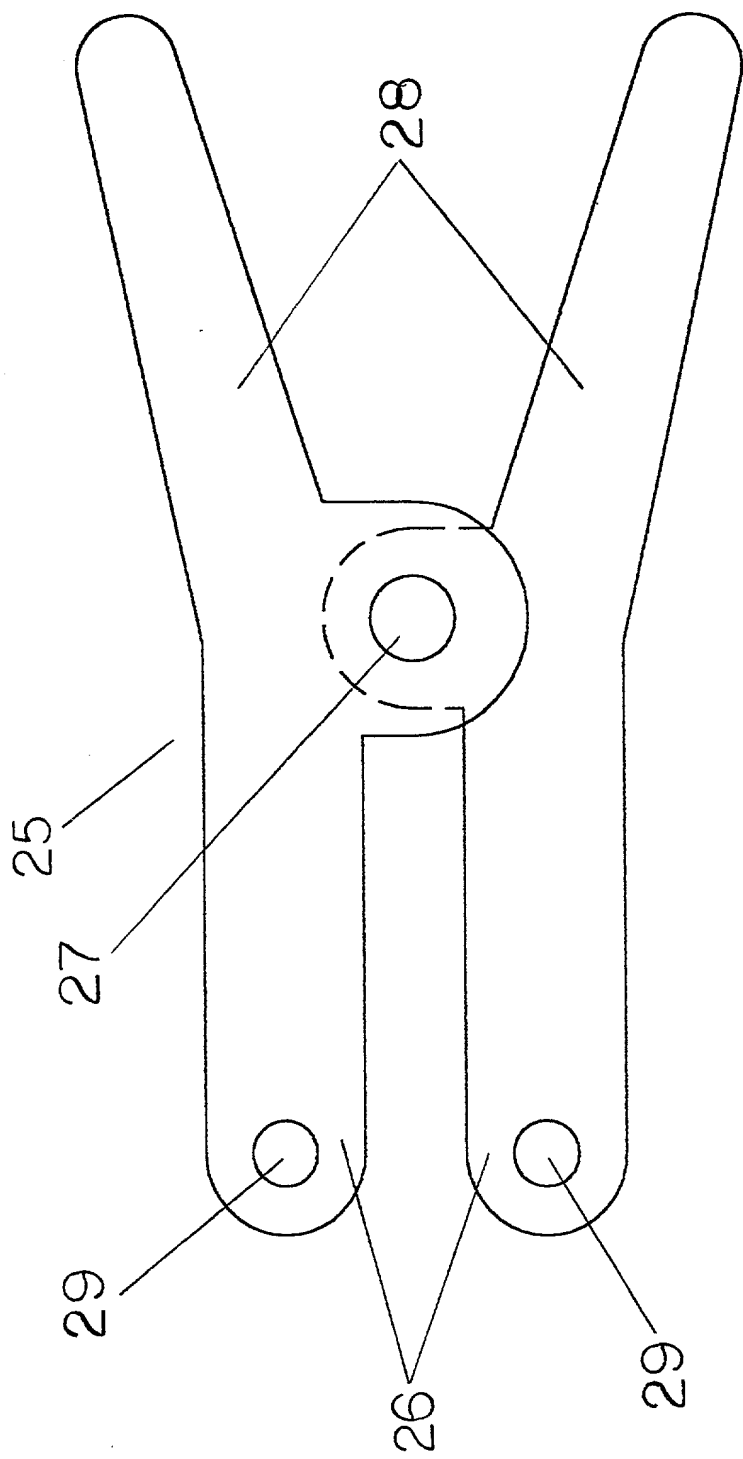


FIG. 5

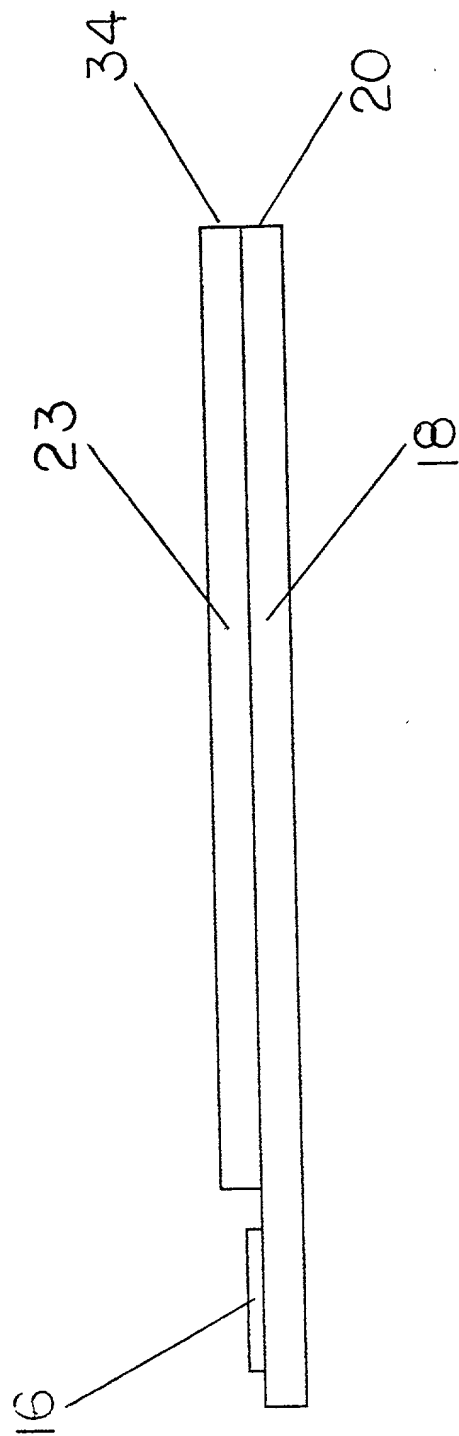
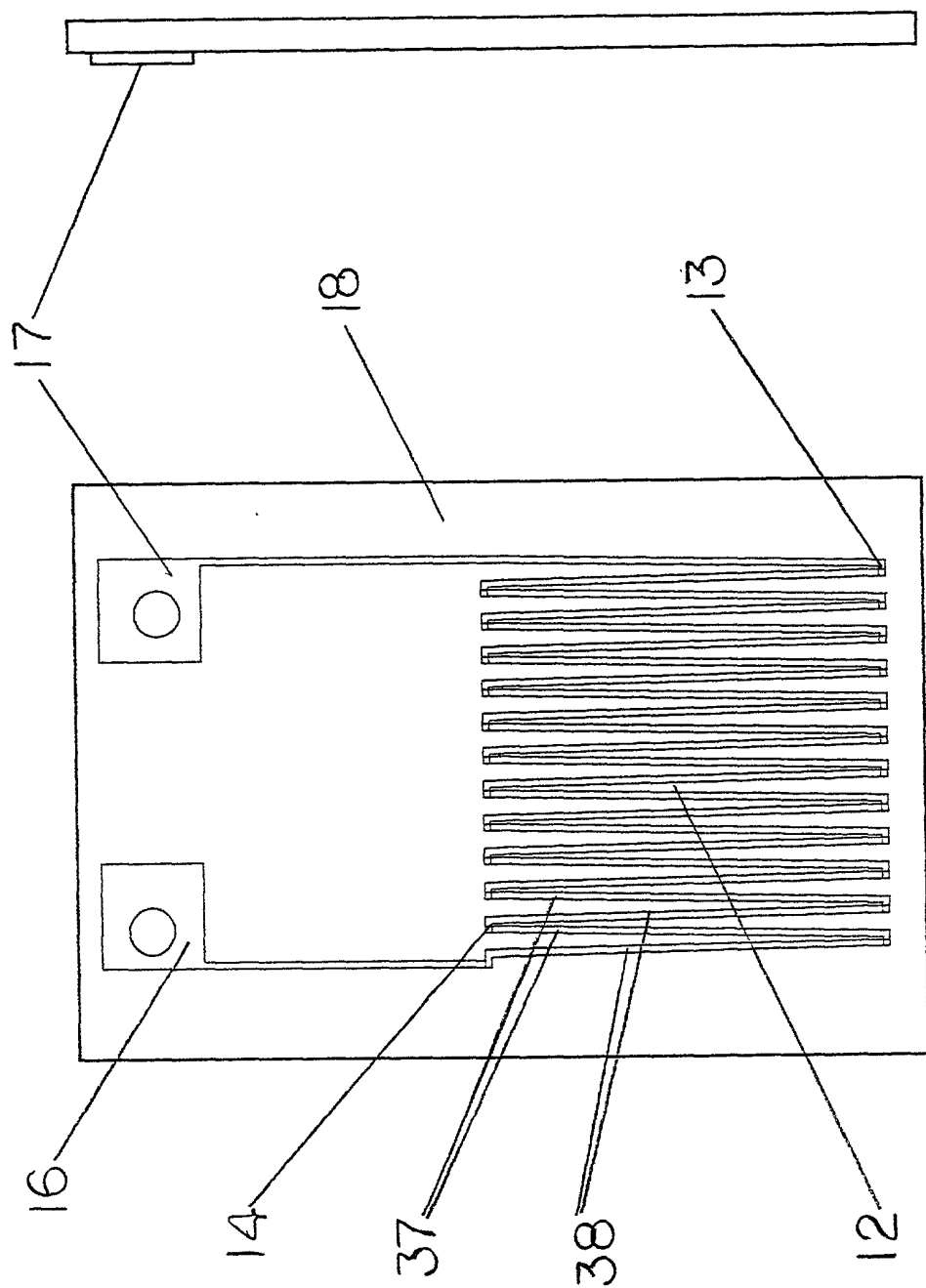


FIG. 6

FIG. 7



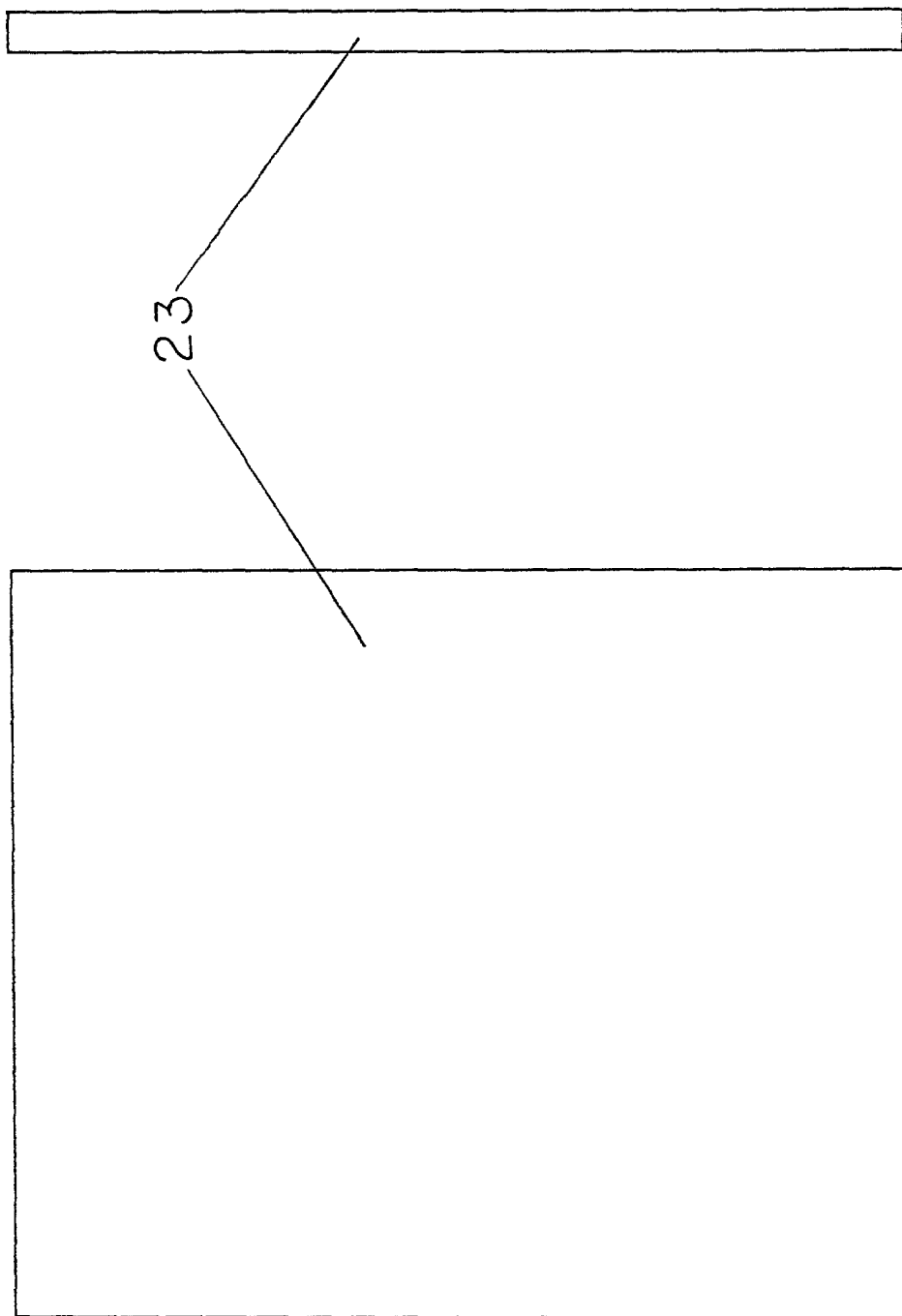


FIG.8